China’s Progress in Semiconductor Manufacturing Equipment

Accelerants and Policy Implications

CSET Policy Brief

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Executive Summary

China has a chip problem. It depends entirely on the United States and U.S. allies for access to advanced commercial semiconductors, which underpin all modern technologies, from smartphones to fighter jets to artificial intelligence. China’s current chip dependence allows the United States and its allies to control the export of advanced chips to Chinese state and private actors whose activities threaten human rights and international security. Chip dependence is also expensive: China currently depends on imports for most of the chips it consumes.

China has therefore prioritized indigenizing advanced semiconductor manufacturing equipment (SME), which chip factories require to make leading-edge chips. But indigenizing advanced SME will be hard since Chinese firms have serious weaknesses in almost all SME sub-sectors, especially photolithography, metrology, and inspection. Meanwhile, the top global SME firms—based in the United States, Japan, and the Netherlands—enjoy wide moats of intellectual property and world-class teams of engineers, making it exceptionally difficult for newcomers to the SME industry to catch up to the leading edge.

But for a country with China’s resources and political will, catching up in SME is not impossible. Whether China manages to close this gap will depend on its access to five technological accelerants:

1. **Equipment components.** Building advanced SME often requires access to a range of complex components, which SME firms often buy from third party suppliers and then assemble into finished SME. Just as chipmakers cannot make chips without access to SME, firms cannot make SME without access to these specialized components (henceforth “SME components”).

2. **Government subsidies.** China will likely provide billions of dollars in subsidies to its SME industry over the next five years. The volume of funding available to Chinese SME firms likely exceeds the fledgling industry’s ability to efficiently absorb that funding.
3. **Explicit knowledge transfers.** Inspection and reverse engineering as well as intellectual property theft will help Chinese firms copy the advances made by top global SME firms.

4. **Tacit know-how transfers.** Approximately 1,100 Chinese nationals currently work at SME firms outside China. These workers return to China at low rates, but the few who do return bring valuable engineering know-how and often secure top jobs.

5. **Collaboration between Chinese SME firms and chip fabrication facilities (fabs).** Refining a product to achieve the high yield and throughput demanded by semiconductor manufacturers takes years and requires extensive testing and feedback from buyers. Chinese SME firms have struggled to find buyers willing to collaborate with them during this refinement process and to bring them revenue to accelerate their growth.

Reducing China’s access to these accelerants will require a significant policy effort, but such an effort may be necessary to sustain China’s chip dependence in the long term. To this end, we recommend the following policies, each aimed at reducing China’s access to one or more of the five accelerants identified above.

**The United States should continue screening foreign investments in SME firms and work with allies to harmonize investment screening practices.** Harmonized investment screening policies will prevent Chinese firms from strategically acquiring companies possessing sensitive tacit and explicit knowledge and transferring that knowledge back to China.

**The United States and its allies should more tightly control the export of technical data to China, but use deemed exports in a more targeted, limited way.** Controls on technical data would reduce China’s access to explicit knowledge. However, the United States should apply deemed export controls more sparingly by expanding exemptions and should process licenses more quickly. This would ensure foreign nationals stay in the United States and
contribute to U.S. industry, while taking a targeted approach to protecting against technology transfer.

**To reduce China’s access to critical components required to build advanced SME, the United States and its allies should impose export controls on these components.** Examples include specialized light sources, laser amplifiers, and optics used to build extreme ultraviolet (EUV) and specialized light sources used to build argon fluoride immersion photolithography tools. China is already attempting to indigenize photolithography components, but robust controls would ensure Chinese firms cannot short-circuit this years-long and expensive process by importing these components instead.

**The United States and its allies should export control advanced SME itself, especially any SME that China has yet to acquire.** These controls would deny China the ability to reverse-engineer SME, an important source of explicit knowledge. Controls on SME would also slow the development of advanced Chinese fabs, weakening China’s semiconductor ecosystem.

**The United States should prioritize assessment of SME component chokepoints, their value-add to SME, and vectors of technology transfer.** These assessments would reveal additional policy options, including further SME components to target with export controls.

**The United States should further study and promote transparency on Chinese government subsidies for SME.** If such subsidies are found to be illegal under World Trade Organization (WTO) rules, the United States can consider challenging them. At a minimum, the United States and its allies can consider reducing export controls if China reduces its subsidy programs.

**The United States and its allies should promote continued SME innovation through investments in R&D and workforce development.** For now, leading SME firms based in the United States and allied countries enjoy an incumbency advantage: unlike Chinese SME firms, they have access to critical product feedback and revenues derived from sales to leading global semiconductor
fabs. Workforce and R&D investments would help accelerate the pace of SME innovation, allowing U.S. and allied firms to sustain their incumbency advantage in the years ahead.
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Introduction

China’s semiconductor manufacturing equipment industry looks in 2020 much like Japan’s SME industry of the mid-1970s. Like China today, Japan had a growing semiconductor industry and a large domestic market for finished chips, but its SME suppliers were weak or non-existent across almost every sub-sector.¹ Japan’s sole strength in SME rested in assembly and packaging tools, where it benefited from lower labor costs, just as China does today.

Over 15 years, Japan’s SME industry grew at an astonishing rate. By the early 1980s, Japanese firms together held almost 20 percent of the global SME market. From 1980 to 1990, the global SME market quadrupled in size, and Japanese firms’ sales increased eightfold over the same period. By 1987, Japanese firms’ technical capabilities rivaled or surpassed U.S. firms in most SME sub-sectors (Table 1). Meanwhile, sales for American firms merely doubled, leaving the United States with 45 percent of the global SME market, neck-and-neck with Japan’s 43 percent.²
Table 1. 1987 assessment of U.S. vs. Japanese SME capabilities

<table>
<thead>
<tr>
<th>Process</th>
<th>Japan lead</th>
<th>U.S.–Japan parity</th>
<th>U.S. lead</th>
</tr>
</thead>
<tbody>
<tr>
<td>Photolithography</td>
<td></td>
<td>◀</td>
<td></td>
</tr>
<tr>
<td>Electron-beam lithography</td>
<td></td>
<td></td>
<td>▲</td>
</tr>
<tr>
<td>X-ray lithography</td>
<td></td>
<td>◀</td>
<td></td>
</tr>
<tr>
<td>Ion implanters</td>
<td></td>
<td></td>
<td>▲</td>
</tr>
<tr>
<td>Chemical vapor deposition</td>
<td></td>
<td>O</td>
<td></td>
</tr>
<tr>
<td>Deposition, diffusion, other</td>
<td></td>
<td>O</td>
<td></td>
</tr>
<tr>
<td>Energy-assisted processing</td>
<td></td>
<td>◀</td>
<td></td>
</tr>
<tr>
<td>Assembly</td>
<td></td>
<td>O</td>
<td></td>
</tr>
<tr>
<td>Packaging</td>
<td></td>
<td>◀</td>
<td></td>
</tr>
<tr>
<td>Test</td>
<td></td>
<td>◀</td>
<td></td>
</tr>
</tbody>
</table>

O U.S. position maintaining ▲ U.S. position declining

Source: Interagency Working Group on Semiconductor Technology, 1987

Many factors contributed to Japan’s success: government subsidies, engineering knowledge gained from blueprints and reverse-engineering of U.S. products, returnee talent trained at U.S. firms, and close collaboration—facilitated greatly by the Japanese government—between Japanese SME firms and Japan’s growing number of domestic chipmakers.

Japan’s remarkable catch-up performance in the 1980s has had long-term effects. Japanese and American firms have lost some market share over the three intervening decades, especially to the Dutch photolithography firm ASML. But Japanese firms today still enjoy 31 percent of the $77 billion global SME market, second only to the United States’ 41 percent.
China hopes to replicate Japan’s success story—and the stakes are high. Starting in 2019, the United States and the Netherlands began to expand export controls on high-end SME, without which it is impossible to manufacture leading-edge computer chips.\(^5\) Deprived of access to advanced SME, China’s $145 billion domestic chip market\(^6\) will remain import-dependent for the foreseeable future—unless it can develop its advanced SME capabilities indigenously.

These new export controls have brought new urgency to China’s long-term effort to develop an indigenous SME industry. For now, China has serious deficiencies in every sub-sector of SME except lower end segments such as assembly, packaging, and cleaning equipment. But China’s industrial plans call for Chinese SME firms to supply at least 50 percent of the value of advanced SME purchased by Chinese chipmakers before 2025. And China hopes to develop a working extreme ultraviolet (EUV) photolithography machine—the SME industry’s most expensive and sophisticated tool—by 2030. To further these goals, China’s government plans to invest billions of dollars in SME firms over the coming decade.

Catching up in SME will be more difficult for China than it was for Japan in the 1980s, when the industry was young. In 1981, a single cutting-edge Micralign 500 lithography stepper sold for $675,000—equivalent to $2 million in 2020—and printed transistors with a precision measured in micrometers (around the diameter of a thread of a spider’s web). Today’s equivalents—the EUV machines made by ASML—sell at more than $120 million per unit and print transistors with precision measured in nanometers (the length a fingernail grows in a few seconds). The industry has become highly consolidated, with five firms—Netherlands-based ASML, Japan-based Tokyo Electron, and U.S.-based Applied Materials, Lam Research, and KLA—capturing 65 percent of the global SME market. Across key subsectors, the number of firms operating at the leading edge has winnowed; for example, only ASML remains state-of-the-art in photolithography (Table 2).\(^7\) Leading SME firms have survived blistering competition and have amassed billions of dollars in capital and world-class engineering teams that benefit from decades of hands-on experience building
and operating ever-more complex machines. These firms have built up large moats of intellectual property: the top five firms obtaining over 2,000 U.S. patents in 2020, with high rates of patenting relative to other industries.\footnote{But catch-up is not impossible. In the mid-1970s it must have seemed unlikely that Japanese firms would soon rival those of the United States. Yet within ten to fifteen years, Japan was second only to the United States in SME; a position it has held ever since. And China today has many of the key ingredients for catch-up that benefited Japan more than forty years ago. Like Japan in the 1970s, China is a major player in the assembly and packaging sub-sector of SME, and has a small but growing presence in most other sub-sectors. Like 1970s Tokyo, Beijing has set out ambitious development goals for its semiconductor industry and has dedicated tremendous resources to achieving them. China will likely spend billions of dollars on its SME industry over the next five years; and its technology transfer infrastructure, which employs more than 60,000 people, has made semiconductor progress a key priority. Perhaps most important, China’s domestic demand for semiconductors is about 35 percent of the world’s total; and in 2016 it had eight times more STEM graduates than the United States and ten times more than Japan—gaps that will continue to grow in the coming years.\footnote{This report assesses the gap between China’s SME industry and the current leading edge; identifies and analyzes five key inputs to China’s SME progress; and suggests policy levers for slowing that progress. It then takes stock of China’s SME industry as a whole, assessing the technological gap between China’s best SME firms and top global firms in the United States, Japan, and the Netherlands. The results reveal serious weaknesses across almost every sub-sector of SME, with particular shortcomings in the photolithography sub-sector. The paper then identifies five key factors that will dictate the rate of China’s progress toward leading-edge SME. It closes with implications of this analysis for policymakers.}}
Chinese equipment firms lag behind the leading edge

Despite Beijing’s past efforts to catch up to the leading edge in the SME industry, there remains a wide technological gap between China’s SME capabilities and those of the top global SME firms. This section assesses the width of that gap across different SME sub-sectors.

It is hard to break into the SME industry. Even firms that have survived decades of blistering competition in the SME industry are today being winnowed away at the leading edge. Consider the photolithography sub-sector: already highly concentrated in 1990 with six leading companies, today just one company—ASML—leads the industry with a monopoly over the latest EUV technology (Table 2).

**Table 2. Photolithography companies at introduction of each node**

<table>
<thead>
<tr>
<th>Type</th>
<th>Light source</th>
<th>Node (nm)</th>
<th>Year mass production</th>
<th>ASML (Netherlands)</th>
<th>Nikon (Japan)</th>
<th>Canon (Japan)</th>
<th>SVGL (U.S.)</th>
<th>Ultratech (U.S.)</th>
<th>Perkin Elmer (U.S.)</th>
</tr>
</thead>
<tbody>
<tr>
<td>i-line</td>
<td>365 nm</td>
<td>800</td>
<td>1990</td>
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<td></td>
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<tr>
<td></td>
<td></td>
<td>600</td>
<td>1994</td>
<td></td>
<td></td>
<td></td>
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<td></td>
<td></td>
</tr>
<tr>
<td>Krypton fluoride (KrF)</td>
<td>248 nm</td>
<td>350</td>
<td>1995</td>
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<td></td>
<td></td>
<td></td>
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<td></td>
<td></td>
<td>250</td>
<td>1997</td>
<td></td>
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<td></td>
<td></td>
<td>180</td>
<td>1999</td>
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<td></td>
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<tr>
<td></td>
<td></td>
<td>130</td>
<td>2001</td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
</tr>
<tr>
<td>Argon fluoride (ArF)</td>
<td>193 nm</td>
<td>90</td>
<td>2004</td>
<td></td>
<td></td>
<td></td>
<td></td>
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<td></td>
<td></td>
<td>65</td>
<td>2006</td>
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<td></td>
<td></td>
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<tr>
<td></td>
<td></td>
<td>45/40</td>
<td>2009</td>
<td></td>
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<td></td>
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<tr>
<td></td>
<td></td>
<td>32/28</td>
<td>2011</td>
<td></td>
<td></td>
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<td></td>
</tr>
<tr>
<td></td>
<td></td>
<td>22/20</td>
<td>2014</td>
<td></td>
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<td></td>
</tr>
<tr>
<td></td>
<td></td>
<td>16/14</td>
<td>2015</td>
<td></td>
<td></td>
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<tr>
<td></td>
<td></td>
<td>10</td>
<td>2017</td>
<td></td>
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</tr>
</tbody>
</table>
The handful of firms remaining at the leading edge in SME are almost exclusively headquartered in three countries—and China is not one of them. U.S., Dutch, and Japanese companies collectively enjoy between 80 and 95 percent market share in almost every sub-sector of the SME industry (see Box 1 for a breakdown of the tools that define the major sub-sectors of SME). China, by contrast, has less than 2 percent market share across almost all SME sub-sectors. Chinese firms have little to no capacity to build a range of critical SME, including tools for photolithography, wafer inspection, advanced ion implantation, atomic layer etching, and testing advanced logic chips. And most Chinese SME firms have virtually no sales outside of China, struggling to sell even to Chinese chipmakers. In all sub-sectors for which data exists, China’s localization rate is lower than 20 percent, meaning Chinese customers buy less than 20 percent of their SME (by value) from Chinese suppliers. Aggregating across sub-sectors, China has localized less than 8 percent of its $14 billion annual domestic demand for SME.

### Box 1. Understanding semiconductor manufacturing equipment

Manufacturing modern computer chips is a highly complex process requiring a wide range of sophisticated semiconductor manufacturing equipment (SME). To begin, **wafer manufacturing** firms produce thin slices of pure silicon called wafers, which they sell to chip fabrication facilities, also known as “fabs.” Fabs then add dozens of layers of nanometer-scale circuitry on the surfaces of these wafers, using a wide range of tools in the process:

- The fabrication process begins with a **deposition** machine, which deposits a layer of material that then hardens on the surface of the wafer.
● A resist processing machine then applies a chemical called a photoresist on top of this hardened substrate.

● Next, a photolithography machine uses a system of mirrors, lenses, and lasers to project beams of light through a photomask—a window intricately patterned with shapes corresponding to transistors and other electronic components—onto the surface of the wafer. The parts of the photoresist that have been exposed through the photomask develop like a roll of film, while parts shadowed by the photomask do not, thus replicating the pattern described by the photomask on the surface of the wafer.

● Etching tools then chemically carve these projections into the hard substrate created during the initial deposition process. (Other times, instead of etching, atoms are embedded into the layer in a process called ion implantation.)

● Finally, the completed layer is flattened in a process called chemical mechanical planarization (CMP), and the process starts over.\textsuperscript{17}

● Throughout fabrication, process control tools inspect the wafer to ensure no errors, while handling tools transport the wafers and photomasks around the fab.

After fabrication, testing tools ensure the fabricated chip operates as intended. Finally, assembly and packaging tools cut dozens of individual chips out of each wafer and add protective and connective structures (called packaging) to the chips so that they can be integrated into products such as smartphones.

China does have two relative strengths in SME. First, like Japan in the 1970s, China’s assembly and packaging equipment sub-sector benefits from relatively low labor costs: Chinese firms hold a considerable 22.9 percent of the global market (Table 3). Second, Chinese firms have produced sophisticated dry etchers, used for advanced logic, memory and packaging, since the early 2000s.\textsuperscript{18} Today, Chinese firm Advanced Micro-Fabrication Equipment (AMEC) advertises etching equipment for semiconductor manufacturing and packaging from 65 nm to 7 nm nodes, though much of its revenue derives from legacy nodes and larger feature sizes.\textsuperscript{19}

These strengths should not be overstated, however. Assembly and packaging require less technological sophistication than other sub-
sectors and comprise just 4.9 percent of the SME market.\(^2\) (And the chip production steps of assembly, test, and packaging account for only 10 percent of a chip’s value.) In etch, China holds just 1.8 percent of the global market, despite recent advances, because AMEC’s etch tools are not suitable for the most difficult parts of the lithography process.

**Table 3. Revenue, market share and bottlenecks of leading Chinese SME firms by sub-sector**

<table>
<thead>
<tr>
<th>SME sub-sector</th>
<th>Leading Chinese firms</th>
<th>China’s total revenue ($M), 2019</th>
<th>China’s % global market share, 2019</th>
<th>China’s most important bottleneck(^2)</th>
</tr>
</thead>
<tbody>
<tr>
<td>Assembly and Packaging</td>
<td>Hoson, Grand Tec</td>
<td>678</td>
<td>22.9%</td>
<td>No major gaps</td>
</tr>
<tr>
<td>Services</td>
<td>various</td>
<td>297</td>
<td>1.9%</td>
<td>-</td>
</tr>
<tr>
<td>Deposition</td>
<td>NAURA, Piotech</td>
<td>235</td>
<td>1.8%</td>
<td>Atomic layer deposition, rapid thermal processing</td>
</tr>
<tr>
<td>Etch</td>
<td>AMEC, NAURA</td>
<td>246</td>
<td>1.7%</td>
<td>Atomic layer etch</td>
</tr>
<tr>
<td>Process Control</td>
<td>RSIC, NAURA, Grand Tec</td>
<td>94</td>
<td>1.4%</td>
<td>Metrology and Inspection</td>
</tr>
<tr>
<td>Chemical mechanical planarization</td>
<td>Hwatsing</td>
<td>20</td>
<td>1.4%</td>
<td>CMP for advanced nodes</td>
</tr>
<tr>
<td>Test</td>
<td>AccoTEST, Hoson, JHT, Grand Tech, Shanghai Juno El, Changchuan</td>
<td>69</td>
<td>1.3%</td>
<td>Testing tools for advanced logic chips</td>
</tr>
<tr>
<td>Lithography</td>
<td>SMEE, Kingsemi</td>
<td>33</td>
<td>0.2%</td>
<td>EUV and ArF immersion photolithography</td>
</tr>
<tr>
<td>Ion Implanters</td>
<td>Kingstone Semi, CETC</td>
<td>-</td>
<td>0%</td>
<td>Ion implanters for advanced nodes</td>
</tr>
<tr>
<td>Wafer mfg., handling, marking</td>
<td>JSG, LanZhou, Beijing JingYi, SMEE</td>
<td>0</td>
<td>0%</td>
<td>Wafer manufacturing and handling</td>
</tr>
<tr>
<td><strong>Total</strong></td>
<td>-</td>
<td><strong>1,672</strong></td>
<td><strong>2.2%</strong></td>
<td>-</td>
</tr>
</tbody>
</table>

Source for revenues and market shares: VLSI Research\(^2\)

At the other end of the spectrum lies China’s weakest sub-sector: lithography, which includes photolithography equipment and other
tools. The top Chinese photolithography firm, Shanghai Micro Electronics Equipment (SMEE), has existed for 18 years, but its most sophisticated tool operates at the 90 nm node, about eight generations (16 years) behind the currently leading 5 nm node. And even SMEE’s 90 nm tool is only a prototype, generating no sales. Building a mass production-ready photolithography tool with low cost, low manufacturing error rates, and high throughput can take years after initial prototyping, suggesting SMEE is even more than 16 years behind the leading edge. Meanwhile, SMEE claims it will introduce a 28 nm ArF immersion tool in 2021 or 2022, but this tool is expected to be unsuitable for mass production, with a throughput likely no more than one third that of commercially-viable counterparts. It may take SMEE additional years to refine the tool for commercial use.

Other parts of the photolithography industry have also posed problems for Chinese firms. SMEE struggles even to produce photolithography steppers: earlier-generation photolithography tools used both for making less advanced chips as well as for the packaging stage of chip production, in which finished wafers are turned into completed chips. Meanwhile, China has no presence in the markets for electron-beam and laser lithography tools. These tools are necessary for creating the photomasks used in photolithography and for low-volume production of chips; and they are made only by U.S., Japanese, German, and Swedish firms.

These severe weaknesses might surprise readers of Chinese news outlets, which tend to exaggerate China’s capabilities in photolithography. In November 2018, for example, Chinese state media claimed that the Institute of Optoelectronics Technology of the Chinese Academy of Sciences developed a 22 nm photolithography tool. The tool in question has low yield and throughput and is suitable for manufacturing chips in small, outdated wafers. Moreover, given its technical parameters, this tool almost certainly cannot be used for making advanced chips.

China’s current capabilities in SME are far below the level targeted in its industrial plans. The “Technical Roadmap” accompanying Beijing’s Made In China 2025 plan set forth a range of ambitious goals for SME localization, including 50 percent localization of 90–
32 nm process equipment by approximately 2018 and 50 percent localization for 90 nm photolithography by 2020.\textsuperscript{32} As of October 2020, localization remains below 10 percent across all SME sub-sectors except packaging and assembly; and China’s leading photolithography firm SMEE has recorded no sales of its 90 nm tool.\textsuperscript{33}

These missed targets suggest that the goals in China’s Technical Roadmap for EUV photolithography—which aim for China to indigenize EUV by 2030—are overly optimistic. The available evidence suggests that skepticism is warranted: In recent years, China has taken over a decade to prototype a given photolithography tool after that tool has first arrived on the global photolithography market (Figure 1). But in the photolithography sub-sector, it often takes years to turn a working prototype into a commercializable machine; indeed, Chinese firms have typically failed to transition from prototype to commercialization. Most recently, its flagship lithography firm SMEE has developed an ArF photolithography tool capable of operating at the 90 nm node 14 years after that node was first introduced. But whether these claims are valid depends heavily on what it means to “reach” a node. China has never managed to transition from a prototype to a machine that captured a significant percentage of the photolithography market.\textsuperscript{34}
Figure 1. The lag between Chinese photolithography prototypes and state-of-the-art commercial photolithography tools, 1978–2030

While catching up in areas like photolithography appears difficult, experts disagree about precisely how long China might take to catch up to the leading edge in SME. In fall 2019, CSET asked five experts to estimate the time until Chinese firms become “internationally competitive” in three key SME sub-sectors. Experts gave a median estimate of ten to fifteen years, with a range of five to 25 years, for photolithography, and five to ten years, with a range of five to twenty years, for both deposition and etch. See Appendix A for more survey details. (China may even fail altogether in building a competitive SME industry.)
This variation in response is justified. First, even if China does not reach the leading edge in deposition, etch, and photolithography by 2030, it still has tremendous room for growth at less advanced nodes. Second, it remains unclear which actions China might take to hasten its progress, and which actions the United States and its allies might take to slow China’s progress. The remaining sections of this paper explore in greater detail the accelerants to China’s progress and how future policy actions might affect China’s access to these accelerants.

**Accelerants to China’s progress**

In order to catch up in the SME industry, Chinese firms will need access to some mixture of all of the following technological accelerants:

1. Extensive government subsidies.
2. Imports of critical SME components.
3. Explicit knowledge derived from blueprints, code, and other sources.
4. Tacit know-how transfers through recruitment of employees at leading global SME firms.
5. Collaboration between Chinese SME firms and semiconductor fabs.

Following is an assessment of China’s current access to each of these components and how each accelerator affects China’s progress in SME. Table 4 summarizes the current state of each of the five accelerants. The following subsections explain each of the five in greater detail.

<table>
<thead>
<tr>
<th>Input</th>
<th>Description</th>
<th>China’s current access</th>
</tr>
</thead>
<tbody>
<tr>
<td>#1: Government subsidies</td>
<td>China’s government guidance funds—particularly the National Integrated Circuit Industry Investment</td>
<td><strong>High.</strong> Chinese government investments in SME are not primarily limited by the volume of investment available, but by poor fund</td>
</tr>
<tr>
<td>#2: Equipment component imports</td>
<td>These components are needed to make SME. Like SME itself, SME components—such as EUV light sources—are often complex, expensive, and made by a single firm. Chinese firms cannot build advanced SME without access to these components.</td>
<td><strong>Moderate.</strong> The export of components specially designed for controlled SME, such as EUV tools, is currently controlled. (However, many other components may not be controlled.) Many component producers may also be subject to exclusive supply agreements with leading SME firms. Therefore, China is attempting to indigenize components, such as for photolithography tools.</td>
</tr>
<tr>
<td>#3: Explicit knowledge transfers</td>
<td>Engineering knowledge recorded in blueprints, patents, and data, or which can be inferred through close inspection and reverse-engineering of a finished product.</td>
<td><strong>Moderate.</strong> China has a long-standing, large-scale, government-run program to transfer explicit technical knowledge of all kinds, including from top global SME firms, by legal, illegal, and extralegal means. These efforts may intensify over the coming decade.</td>
</tr>
<tr>
<td>#4: Tacit knowledge transfers</td>
<td>Hard-to-articulate engineering knowledge that resides in the minds of experienced engineers rather than in written documents. This knowledge could be transmitted to Chinese firms via talent poached from top global SME companies.</td>
<td><strong>Low.</strong> About 1,100 Chinese nationals currently work at top global SME firms outside China. Only a limited stock of returnee engineers—numbering only in the dozens—have joined Chinese SME firms. High-skilled Chinese engineers working abroad currently return to China at low rates, suggesting limited scope for tacit know-how transfers unless this changes. Chinese firms could also recruit engineers from Chinese sites at non-Chinese SME firms. China has also had some success recruiting engineers from Japan and South Korea.</td>
</tr>
<tr>
<td>#5: Collaboration between Chinese SME firms and fabs</td>
<td>It can take years to refine a prototyped SME into a tool that achieves sufficient yield and throughput to satisfy manufacturers. This refinement process benefits greatly from collaboration with fabs, which offer crucial product feedback.</td>
<td><strong>Low.</strong> The world’s leading fabs (Intel, Samsung, and TSMC), largely do not collaborate with Chinese SME firms, with few exceptions, such as TSMC’s purchase of some of AMEC’s etch equipment. Even Chinese fabs depend on non-Chinese SME.</td>
</tr>
</tbody>
</table>

**Accelerant #1: Government subsidies**

China’s funding for its semiconductor industry, and SME firms specifically, flows through government-owned funds and enterprises. The most notable government guidance fund is the National Integrated Circuit Industry Investment Fund, also called the “Big Fund,” which has thus far invested $950 million in Chinese
SME firms. However, most subsidies are in the form of equity investments by local and central state-owned enterprises or government holding firms. These investments represent critical, albeit often wasteful and poorly managed, subsidies for firms competing in the capital- and R&D-intensive SME industry.

The Big Fund is indeed big. In its first financing round in 2014, the fund raised 138.7 billion yuan ($21.8 billion in 2014 USD) and had registered capital of 98.7 billion yuan ($15.5 billion in 2014 USD). By the end of 2017, the fund had made strategic investments in 67 projects, of which 24 were publicly traded companies. By August 2018, when the first investment phase concluded, it had invested in 70 companies. These are equity investments—not pure subsidies. But for firms operating in the capital-intensive semiconductor industry, a consistent source of billions of dollars in capital injections constitutes a major advantage.

The establishment of the Big Fund also spurred the creation of numerous sister funds injecting capital at the provincial and city levels. By December 2017, the total size of Big Fund-driven sub-funds either proposed or established by various localities purportedly exceeded 300 billion yuan (about $45 billion in 2017 USD). An April 2018 estimate suggests that local funds in cities including Beijing, Shanghai, Shenzhen, Hubei, and Anhui amounted to more than 500 billion yuan (about $80 billion in 2018 USD). Chinese industry participants projected combined funding from national and local funds to reach 1 trillion yuan in 2020 (about $150 billion in 2020 USD).

But the impressive scale of these investments comes with three important caveats. First, while local funds collectively dwarf the Big Fund, they tend to be wasteful and risk-averse. Local governments often appoint inexperienced bureaucrats to manage their funds or impose constraints on these funds, limiting their effectiveness. Local funds also tend to avoid investing in smaller, newer companies, with only a minority of funds going toward venture capital investments.

Second, China’s investments have overemphasized semiconductor fabrication at the expense of funding SME firms and innovative
startups. Table 5 breaks down Big Fund investments across the semiconductor supply chain in Big Fund Phase I. Counting early investments during phase II, the Big Fund has provided $950 million in funding to China’s SME industry. Chinese analysts have argued that compared with etching equipment and thin film equipment, the localization rates of silicon wafer manufacturing equipment, lithography equipment, ion implanters, and measurement equipment are low, and that these technologies deserve greater fund support. Investments from local funds and state-owned enterprises are more difficult to track, and likely account for most SME subsidies. However, some of these investments may likewise be skewed toward mature subsectors, as they are more fiscally constrained and less likely to make long-term bets on strategic technologies like SME.

Table 5. Big Fund Phase 1 investment across the semiconductor supply chain

<table>
<thead>
<tr>
<th>Segment of supply chain</th>
<th>Percent of total investment</th>
<th>Estimated investment (USD)</th>
</tr>
</thead>
<tbody>
<tr>
<td>Manufacturing</td>
<td>63</td>
<td>13.7 billion</td>
</tr>
<tr>
<td>Design</td>
<td>20</td>
<td>4.3 billion</td>
</tr>
<tr>
<td>Assembly, test, and packaging</td>
<td>10</td>
<td>2.1 billion</td>
</tr>
<tr>
<td>SME and materials</td>
<td>7</td>
<td>1.5 billion</td>
</tr>
</tbody>
</table>

Source: Various

Third, and perhaps most important, it remains uncertain how much funding China’s weakest SME firms are capable of absorbing. Wei Jun, vice president of the Big Fund, has noted the backwardness of Chinese materials and equipment companies is so profound that it could even be difficult to find suitable investment targets. State-run media admits that while the Big Fund has promoted Chinese SME companies like AMEC, NAURA, and National Silicon Industry Group, this has not yet allowed such companies to break into the higher end, nor significantly lowered dependence on SME imports. Firms like SMEE, which earns between 10 and 40 million
dollars in revenue per year, are unlikely to attract or effectively absorb billions of dollars in funding in the near term.\textsuperscript{52}

The second round of Big Fund investments (Box 2) appears to be targeting more strategically important parts of the semiconductor supply chain, including SME. For more mature SME firms capable of absorbing large investments, government funding appears to be ample.

Box 2: The second round of the Big Fund

News of the second round of the Big Fund approximately coincided with the annual government work report of 2018, in which promotion of the integrated circuit industry, for the first time, rose to the top of the list of Beijing’s proposed economic policies.\textsuperscript{53} Beijing has long viewed semiconductors as a strategic technology, but the last few years of trade tensions have driven home the extent of China’s dependence on a range of technologies concentrated in the United States and allied countries. Big Fund president Ding Wenwu’s public remarks on the second round of the Big Fund in 2018 highlighted the need to boost the semiconductor design, equipment, and materials industries “as much as possible.”\textsuperscript{54}

By May 2020, the second round of the Big Fund had $6.5 billion in registered capital.\textsuperscript{55} $2.2 billion of this capital will go to Semiconductor Manufacturing International Corporation (SMIC), China’s leading semiconductor manufacturer; but other investments appear to be more evenly distributed across the supply chain, including smaller, emerging companies. In SME specifically, likely focus areas include etching machines, thin film equipment, and test and cleaning equipment, along with a focus on accelerating investment into core equipment such as lithography machines and chemical mechanical planarization equipment.\textsuperscript{56}

Access to capital is unlikely to be a key constraint for firms operating in the SME industry going forward. Indeed, when CSET asked experts in the semiconductor industry whether “a change in China’s access to money and funding” would impact their estimates of China’s timeline to becoming internationally competitive in SME, five out of seven respondents said “probably
Instead, Chinese SME firms will likely be bottlenecked in other areas, as discussed below.

**Accelerant #2: Equipment component imports**

Alongside SME itself, certain specialized SME components—parts used to make SME—present chokepoints underexploited by U.S. and allied export controls, likely because they are buried deep in the semiconductor supply chain. Consider:

1. Apple’s iPhone is made from a range of parts sourced from more than 200 companies from around the world—including advanced chips made by TSMC.

2. To build these chips, TSMC relies on hundreds of input technologies, including advanced SME. TSMC cannot make the most advanced iPhone chips without EUV photolithography tools provided by ASML, for example.

3. To produce SME such as EUV photolithography tools, firms like ASML are themselves dependent on specialized SME component manufacturers.

One option for slowing down China’s progress toward advanced SME is to export control the components required for making SME. These components, like SME itself, are often highly complex and developed only by a handful of firms. For example, EUV photolithography scanners, which are essential for manufacturing leading edge chips, incorporate a range of highly complex components manufactured exclusively by firms based in the United States and allied countries. Critical components of ASML’s EUV photolithography scanners include:

- Complex systems of mirrors sold only by German optics firm Zeiss.
- Laser amplifiers sold only by the German firm Trumpf.
- A light source provided only by Cymer, an American subsidiary of ASML.
While a few other firms could develop alternatives to these three EUV components, those firms also tend to locate in the United States and allied countries.\textsuperscript{59} Argon fluoride immersion photolithography tools—sold only by ASML and Nikon—likewise rely on specialized components, including light sources provided only by Cymer and Gigaphoton. Further research could likely identify additional components required for manufacturing advanced SME: ASML alone relies on thousands of component suppliers to make its EUV photolithography tools.\textsuperscript{60}

China currently has variable access to SME components. The export of components “specially designed” for controlled SME—which include EUV tools—is currently controlled.\textsuperscript{61} Additionally, many component producers may be prevented from selling to China if they are bound by exclusive supply agreements with leading SME firms.\textsuperscript{62} On the other hand, unspecialized components or components specially designed for uncontrolled SME may not be controlled. Some Chinese analysts have argued that an inability to access key components of photolithography equipment is the single biggest reason China has fallen so far behind in this sub-sector.\textsuperscript{63} In the meantime, China is attempting to indigenize components, especially for photolithography tools.

The risks of export controls may continue to drive China to indigenize SME components rather than import them where possible. However, China’s prospects for indigenizing SME would slow if the United States and its allies are willing to ensure export controls on the critical components required for assembling advanced SME and whether policymakers are able to identify additional critical SME components to target with export controls.

### Accelerant #3: Explicit knowledge transfers

The engineering knowledge required for building complex SME can be divided into explicit knowledge and tacit know-how. Explicit knowledge refers to engineering knowledge that is recorded in blueprints, patents, and data, or which can be inferred through close inspection and reverse-engineering of a finished product. It is distinct from tacit know-how: hard-to-articulate engineering knowledge that resides in the minds of experienced engineers.
rather than in written documents. This section explores the likelihood and consequences of China gaining access to explicit knowledge; the next focuses on tacit know-how transfers.

It appears likely that China will gain both lawful and unlawful access to some of the explicit knowledge required to build leading-edge SME. The most readily-accessible explicit—and legal—knowledge resides in patents or patent applications, which typically publish no later than 18 months after filing and include high-level blueprints for inventions. Typically, some but not all elements of an invention described in an active patent are also legally protected: Chinese firms can licitly build the unprotected elements immediately. Firms must wait twenty years to make legal use of the protected elements. But even expired patents contain some knowledge that remains relevant to building leading-edge systems. For example, ASML began to develop EUV in earnest during the early 2000s, meaning that some of its oldest EUV-relevant patents will begin to expire soon, giving Chinese firms unrestricted access to these innovations.

Another licit path to gaining explicit SME engineering knowledge is reverse-engineering equipment sold by top firms. This is exceptionally difficult and requires buying and then disassembling expensive equipment, likely destroying much of the equipment’s value in the process. Nevertheless, reverse-engineering has played a role in various states’ efforts to replicate American technologies, including China’s effort to build a competitor to the F-22 fighter jet and Japan’s effort to catch up in the SME industry in the 1970s and 1980s. Indeed, Japan’s VLSI project, which yielded 1,000 patents between 1976 and 1979, spent between one third and one half of its funds to purchase advanced U.S. production equipment, much of which was simply dismantled and analyzed for reverse engineering. Of course, the complexity of equipment has exponentially increased over the past four decades, making this strategy more difficult today.

China will also likely allow IP theft of proprietary blueprints and data from leading SME companies. This proprietary data contains knowledge that is often more detailed, product-specific, and valuable than what is publicly disclosed in patents. When China
prioritizes a given high technology, cases of IP theft involving Chinese nationals often follow. For example, jet engines have featured prominently in its Eleventh, Twelfth, and Thirteenth Five Year Plans. In the past decade, the United States has accused a number of hackers and company insiders of working to steal intellectual property related to jet engine manufacturing from American firms.

Chinese actors have already stolen some SME-related explicit knowledge: former ASML employees allegedly stole documents containing trade secrets, possibly including source code, software, price strategies, and secret user manuals, and transferred them to Chinese companies. In 2019, ASML won an $845 million judgment against the (now bankrupt) Chinese lithography firm XTAL for this misconduct. Hacking groups with connections to mainland China have also targeted other parts of the semiconductor supply chain.

Given the risk of IP theft, SME firms have strong incentives to compartmentalize proprietary data and other forms of explicit engineering knowledge. SME firms make heavy use of non-disclosure agreements, cybersecurity measures, and other written rules governing information sharing to prevent explicit knowledge and tacit know-how diffusion. Many firms also refuse to open R&D sites in China due to concerns about weak IP protections and the potential for know-how transfers, while those that do open R&D sites take precautions such as sharing only low-end processes and technologies while keeping advanced functions outside of China. Indeed, ASML noted in the aftermath of the XTAL case that “there is no blueprint on the street where you can build a lithography machine.”

**Accelerant #4: Tacit know-how transfers**

As mentioned in the previous section, knowledge can transfer explicitly or tacitly. We focus here on tacit know-how: hard-won engineering knowledge, acquired through hands-on training and trial-and-error, which is near-impossible to write down and instead resides only in the minds of researchers and engineers. China’s progress in SME depends heavily in recruiting these experts. One approach is for China to convince many of the approximately 1,100
Chinese employees of top SME firms working outside of China to return to China (as this section will later discuss relative to Table 6).

SME has become extraordinarily difficult to copy. Even in the 1970s and 1980s, when SME was much simpler, Japanese firms seeking to catch up to the United States already relied much more on tacit rather than explicit knowledge transfers.\textsuperscript{74} Decades later, SME is more complex than ever: building leading-edge tools requires innumerable design choices that depend on judgment calls, educated guesses, and years of hands-on experience.\textsuperscript{75} Getting some of these choices right is difficult; but getting all of them right is necessary in order to achieve high yields.\textsuperscript{76} Engineers learn to navigate these choices through extensive experimentation, manipulation, prototyping, testing, and refinement. And while the participation of a single star scientist can be helpful,\textsuperscript{77} it takes a tightly integrated, highly experienced team to achieve consistent innovation.\textsuperscript{78} It is nearly impossible to record or steal the engineering knowledge embedded in a highly productive team of SME developers.\textsuperscript{79}

This is the problem of tacit technological know-how. Consider the case of the U.S. F-22.\textsuperscript{80} As combat aircraft have become more complex, from hundreds of parts in the 1930s to 300,000 in the 2010s, they have also grown in the tacit know-how required to develop them. China has struggled for years to produce its own competitor to the F-22 despite having benefited from extensive transfers of explicit knowledge via cyber operations, traditional industrial espionage, training from Russian aviation experts, and purchases of many critical components for reverse-engineering. China’s J-20 Black Eagle, commissioned in 2017, remains inferior to the F-22, with a range of design flaws and an underpowered engine. The greatest hurdle for China in replicating the F-22 has not been a lack of blueprints or other explicit knowledge, but rather a lack of tacit and organizational know-how.\textsuperscript{81}

The same is true of semiconductor manufacturing equipment, the most sophisticated of which can be comparable in complexity to an F-22.\textsuperscript{82} CSET asked seven semiconductor industry experts to rate the importance of four resources commonly thought to be important inputs to developing sophisticated SME. On average,
experts rated “talent with technical know-how” as the most important input; about as important as investment, access to blueprints, and access to SME for inspection combined. CSET also asked these seven experts whether a “change in China’s access to talent” would alter their estimates of China’s timeline to international competitiveness in SME. Four out of seven respondents said “Definitely yes,” and the remaining three said “Probably yes.” (See Appendix A for more details.)

In the case of EUV, the tacit know-how problem is especially serious. ASML’s equipment scans each photomask multiple times per second with 2-nm precision. A Chinese firm could purchase certain key EUV components such as light sources, optics, and laser amplifiers; but achieving the requisite level of mechanical precision requires developing and integrating a multitude of metrology and robotic systems—a tremendous challenge, which virtually requires the help of talent trained at either ASML or its Japanese competitor Nikon.

Chinese engineers working at top firms do not return to China at high rates, but the few who do can have significant impacts. Consider the case of Dr. Zhiyao Yin, co-founder of leading Chinese etch equipment manufacturer AMEC. Yin worked at companies such as Intel, Lam Research, and Applied Materials for two decades before returning to China to help advance domestic dielectric etching machines to near-world-leading levels.83 Within a year of launching, AMEC announced the production of chemical vapor deposition and etch products. In a lawsuit filed in 2007, Applied Materials alleged that Yin, Aihua Chen, former general manager of Applied’s CVD product group and co-founder of AMEC, and 30 other former Applied Materials employees had misappropriated trade secrets and breached contracts.84

Chinese firms also have other options for recruiting SME talent. One is to hire engineers from Chinese subsidiaries of non-Chinese SME firms—though this vector presents limited upside, as leading SME firms do not necessarily keep their most advanced operations in China. China could also recruit engineers from Japan and South Korea—a technique the country has already pursued with some success to build its semiconductor industry.
While an in-depth assessment of the potential for tacit know-how transfers to China is beyond the scope of this paper, we can get some sense of the risk by simply tallying the number of Chinese engineers currently employed outside China at top global SME firms using LinkedIn data. Table 6 lists the number of workers with LinkedIn profiles who work for four of the five top SME firms by revenue. Overall, 1.3 percent of these workers studied at Chinese universities, and most appear to be skilled technical workers. CSET estimates that all SME firms headquartered outside China employ a total of approximately 84,000 workers, implying approximately 1,100 total Chinese employees of top SME firms working outside of China.

Table 6. Number of Chinese workers employed by top SME firms outside China

<table>
<thead>
<tr>
<th>Company</th>
<th>Chinese workers employed outside China</th>
<th>Total workers</th>
<th>Percent Chinese workers employed outside China</th>
</tr>
</thead>
<tbody>
<tr>
<td>Applied Materials</td>
<td>260</td>
<td>20,920</td>
<td>1.24%</td>
</tr>
<tr>
<td>KLA</td>
<td>123</td>
<td>7,111</td>
<td>1.73%</td>
</tr>
<tr>
<td>Lam Research</td>
<td>124</td>
<td>8,696</td>
<td>1.43%</td>
</tr>
<tr>
<td>ASML</td>
<td>260</td>
<td>20,700</td>
<td>1.26%</td>
</tr>
<tr>
<td>CSET extrapolation for all non-Chinese SME firms</td>
<td>1,100</td>
<td>84,000</td>
<td>1.3%</td>
</tr>
</tbody>
</table>

Source: CSET analysis using LinkedIn

The vast majority of high-skilled Chinese engineers working at top global semiconductor firms likely will not return to China for work unless immigration or export control rules force them to do so. Previous CSET analysis of NSF data shows that among Chinese Ph.D. students in semiconductor-relevant fields like electrical, electronics, and computer engineering, 90 percent tend to pursue jobs in the United States following graduation from American universities. In the longer term, stay rates remain high: a 2018 report by the Oak Ridge Institute for Science and Education found that among Chinese science and engineering Ph.D. graduates from American universities, 86 percent remained in the United States ten years after graduation. While CSET has not conducted similar
analyses of the Netherlands and Japan, stay rates may be high in those countries as well.

Nevertheless, some workers do return home, and these returnees often secure top jobs at Chinese SME firms. For example, among the 274 employees with LinkedIn profiles at AMEC, China’s leading supplier of etch tools, 32 have worked previously for top SME firm Applied Materials, an American company. AMEC has approximately 700 employees, so this likely underestimates the total number of Applied Materials alumni now working at AMEC. But these 32 employees are credentialed and well-placed within AMEC: among them are three vice presidents; two senior directors; one general manager; one global product manager; and one associate director.  

Overall—in the absence of counterproductive policies that actively drive away Chinese talent currently employed by top global SME firms—returnee rates will likely remain low. Thus, China’s SME firms, especially those further behind the cutting edge than AMEC, will struggle to gain access to the tacit know-how embodied in workers at top global firms. But a scenario where China recruits one thousand returnees with experience at top firms—with hundreds from ASML for photolithography—could have a large effect on China’s SME progress.

**Accelerant #5: Collaboration between Chinese SME firms and fabs**

Collaboration with top global semiconductor fabs would not only help Chinese SME firms catch up to the leading edge: it could allow them to keep up as the industry continues to innovate. While key components, subsidies, and explicit and tacit know-how can all help Chinese SME companies adapt innovations from top global firms more quickly, collaboration with top fabs (in combination with those accelerants) would help them keep pace with competitors. China would not only build a commercializable EUV photolithography tool on a shorter timeline; it would also be well-positioned to continue to close the gap in the years following.
In the SME industry, transforming a prototype into a commercially successful machine is a years-long process, requiring close collaboration between SME providers and the fabs they sell to.\textsuperscript{91} For example, ASML shipped its first EUV machine in 2010 and spent ten years working closely with chipmakers like TSMC, Intel, and Samsung to refine their EUV machines until they achieved satisfactory yields and throughput for mass production in 2020. As noted previously, China has a long history of developing prototypes of advanced photolithography equipment relatively quickly, but never achieving sufficient yield and volume for successful commercialization.

Even in cases where China has succeeded in commercializing a given tool, that success has often been temporary. For example, Figure 2 shows that in 2011, SMEE began earning revenue from a KrF stepper, more advanced than its workhorse i-line machine. But it discontinued shipments just three years later after struggling to attract sufficient enthusiasm from either domestic or foreign buyers. Meanwhile, revenue from its i-line steppers has declined considerably lately, from a peak of $30.3 million in 2015 to $19.1 million in 2019, and these steppers are used only for lower-end chip packaging, not chip fabrication. While such fluctuations are not uncommon in the competitive SME industry, they underscore the difference between building a tool that works in principle, and building one that satisfies manufacturers. The latter requires years of refinement and collaboration with fabs, and fabs are selective about which suppliers they collaborate with.\textsuperscript{92}
Chinese SME firms have struggled to gain substantial interest from top global fabs thus far. In a notable exception, AMEC, China’s leading etch firm, has managed to secure Taiwan’s TSMC as a buyer. But while other leading chipmakers such as Intel, Micron, and Samsung have worked with Chinese firms to qualify their SME, particularly in search of better prices, they have yet to become serious customers. Most Chinese firms have struggled to sell their SME even to Chinese fabs, which are less advanced than global leaders. For example, SMIC—China’s top chipmaker, which remains multiple generations behind the leading edge—still largely buys its equipment from non-Chinese SME firms. China could use protectionist policies to force Chinese fabs to buy Chinese SME, but has not done so, given the risk of undermining China’s efforts to catch up to the leading edge in chip manufacturing.

Source: VLSI Research
Instead, China has recently begun a new strategy: R&D fabs aimed at qualifying Chinese SME for mass production. At least four such projects are announced or underway, with SMIC announcing one such facility in 2020. This approach represents Chinese SME firms’ second-best option if they cannot gain the business of the world’s leading chipmakers.

Still, challenges remain for Chinese SME firms to gain substantial business. As discussed above, Chinese firms seeking to develop more advanced SME will likely run afoul of the extensive patents held by top global firms, which last for two decades from the time of filing. Chinese courts have been increasingly tough on patent infringement, accepting over four times as many cases in 2019 as they did in 2010 without discriminating against foreign plaintiffs. And even if China were tempted to turn a blind eye to patent infringement by SMIC, the United States, Taiwan, and South Korea—home to the world’s three leading-edge chipmakers—will not. This is a problem for two reasons. First, these firms lead the world in chip manufacturing, and can therefore offer uniquely valuable product feedback on the most sophisticated SME. Second, developing and building high-end SME is exceptionally R&D- and capital-intensive. Revenue from sales to top chipmakers will be critical if Chinese SME firms are to achieve financial independence from Chinese government subsidies.

The need for collaboration with fabs creates a Catch-22 for China’s SME firms. The surest way to catch up to the leading edge in SME is to secure revenue and product feedback through sales to advanced fabs; but advanced fabs will not buy equipment from SME firms unless those firms are already at the leading edge. Thus far, most Chinese SME firms have not found a way out of this bind. China’s best hope to qualify Chinese SME for mass production may be its new strategy of R&D fabs.

**Recommendations**

The previous section of this paper identified five accelerants that could substantially influence China’s timeline for reaching the current cutting edge in SME: access to key components, government subsidies, explicit knowledge and tacit know-how,
and collaboration with advanced fabs located in the United States, South Korea, and Taiwan. The United States and its allies have policy options for affecting each of these accelerants. This section discusses seven options (not necessarily in order of importance), summarized in Table 7 according to how they affect each of the five accelerants to China’s SME progress.

Table 7. Recommendations for slowing China’s SME progress

<table>
<thead>
<tr>
<th>Accelerants</th>
<th>Government subsidies</th>
<th>Imports of SME components</th>
<th>Explicit knowledge transfers</th>
<th>Tacit know-how transfers</th>
<th>Collaboration between Chinese SME firms and fabs</th>
</tr>
</thead>
<tbody>
<tr>
<td>Screen investments in SME firms</td>
<td></td>
<td></td>
<td>✓</td>
<td>✓</td>
<td></td>
</tr>
<tr>
<td>Impose export controls on SME technical data and deemed exports</td>
<td></td>
<td></td>
<td>✓</td>
<td>✓</td>
<td></td>
</tr>
<tr>
<td>Impose export controls on equipment</td>
<td></td>
<td></td>
<td>✓</td>
<td></td>
<td></td>
</tr>
<tr>
<td>Impose export controls on critical SME components</td>
<td></td>
<td>✓</td>
<td></td>
<td></td>
<td></td>
</tr>
<tr>
<td>Prioritize assessment of SME component chokepoints and vectors of knowledge transfer</td>
<td></td>
<td>✓</td>
<td>✓</td>
<td>✓</td>
<td></td>
</tr>
<tr>
<td>Study and promote transparency on China’s SME subsidies</td>
<td>✓</td>
<td></td>
<td></td>
<td></td>
<td></td>
</tr>
<tr>
<td>Promote equipment innovation through R&amp;D and workforce investments</td>
<td></td>
<td></td>
<td></td>
<td></td>
<td>✓</td>
</tr>
</tbody>
</table>

The subsections that follow each take a single row (in order) from Table 7 and explain what the policy recommendation in that row is and how it impacts the highlighted accelerant(s).
Screen investments in equipment firms

<table>
<thead>
<tr>
<th>Accelerants Policies</th>
<th>Government subsidies</th>
<th>Imports of SME components</th>
<th>Explicit knowledge transfers</th>
<th>Tacit know-how transfers</th>
<th>Collaboration between Chinese SME firms and fabs</th>
</tr>
</thead>
<tbody>
<tr>
<td>Screen investments in SME firms</td>
<td></td>
<td></td>
<td>✓</td>
<td>✓</td>
<td></td>
</tr>
</tbody>
</table>

To reduce the risk of explicit knowledge and tacit know-how transfers, the United States and its allies should better collaborate to block Chinese SME firms’ investments, joint ventures, and mergers and acquisitions targeting leading global SME firms. The United States screens sensitive investments in SME relatively carefully, but should continue improving its system to better identify any concerning transactions. But U.S. allies often have more lax investment screening policies that should be coordinated and strengthened.

The threat of technology transfer—particularly semiconductor-related transfers—has shaped the development of the modern U.S. investment screening regime since its establishment in 1988. The Committee on Foreign Investment in the United States (CFIUS) is an interagency committee that reviews transactions by foreign entities to determine whether they raise national security concerns. The U.S. president blocks transactions on recommendations from CFIUS and has already done so for multiple cases involving SME firms. CFIUS was first empowered to screen foreign investments largely in response to the rapid growth of Japan’s semiconductor industry and Japanese firm Fujitsu’s attempted acquisition of the American firm Fairchild Semiconductor. In 2018, the Foreign Investment Risk Review Modernization Act (FIRRMA) broadened CFIUS’ powers and created mandatory notification requirements for investments involving critical technologies, including semiconductors and semiconductor manufacturing equipment, largely due to concerns about Chinese investments. As a result of these changes, U.S. investment screening policies are relatively strong.
The United States can improve these policies. First, it should ensure even all non-controlling Chinese investments in SME firms are captured. Second, it should avoid relying only on export control lists, which currently do not sufficiently capture advanced SME, and also would not include capture early-stage SME R&D for which export controls may be premature or unwarranted.\(^{100}\)

The United States should also engage its allies to ensure the implementation of strong and coordinated investment screening policies. Not all U.S. allies have developed their own investment screening bodies, and some use their powers much more actively than others.\(^{101}\) But both Japan and the Netherlands have recently updated or created investment screening policies, perhaps in part due to the passage of FIRRMA, which requires U.S. engagement with allies on investment screening.\(^{102}\) The CHIPS for America Act, included in the National Defense Authorization Act for Fiscal Year 2021, includes additional provisions to promote harmonized investment screening for semiconductors.\(^{103}\) Still, allied measures have been insufficient. For example, since mid-2019, Chinese investors announced or were reportedly in talks for planned acquisitions of controlling stakes—worth $125 million—in five Japanese firms selling semiconductor technologies, three of which sell SME.\(^{104}\) Therefore, the United States should continue engaging allies and incentivize them to harmonize their investment screening policies, such as by expediting CFIUS investment screening for firms and individuals based in allied countries.\(^{105}\)

**Impose export controls on equipment and its inputs**

We recommend three forms of export controls below, each with complementary strengths:

1. Multilateral export controls on **equipment technical data and deemed exports** would control the transfer of know-how, both in the form of technical data and tacit know-how acquired by employees.

2. Multilateral export controls on **critical equipment components**—such as mirrors, light sources, and laser
amplifiers used to make EUV photolithography tools—would raise barriers to Chinese firms assembling SME.

3. Multilateral export controls on SME itself would reduce the risk of reverse engineering.

**Equipment technical data and deemed exports**

<table>
<thead>
<tr>
<th>Accelerants</th>
<th>Government subsidies</th>
<th>Imports of SME components</th>
<th>Explicit knowledge transfers</th>
<th>Tacit know-how transfers</th>
<th>Collaboration between Chinese SME firms and fabs</th>
</tr>
</thead>
<tbody>
<tr>
<td>Policies</td>
<td></td>
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</tr>
<tr>
<td>Impose multilateral export controls on SME technical data and deemed exports</td>
<td></td>
<td></td>
<td>✓</td>
<td>✓</td>
<td></td>
</tr>
</tbody>
</table>

The United States and its allies should more tightly control the export of technical data for key SME to prevent transferring engineering knowledge to China. However, the United States should more narrowly tailor deemed export controls—applying exemptions where possible and processing licenses more quickly. These policies would balance the benefits of ensuring foreign nationals stay in the United States and contribute to U.S. industry against the risks of technology transfer.

The differences between these two types of controls is subtle but important: whereas data controls would prohibit any individual from bringing technical knowledge back to China, deemed exports can prohibit only foreign nationals from working at SME firms outside their home country that could impart controlled technical knowledge.

Export controls on technical data have the advantage of applying to any individual, regardless of nationality, but the disadvantage of being difficult to enforce. These controls prevent only the export—not the acquisition—of technical data. It is difficult to detect when an individual worker has transferred knowledge to China. Even when such transfers are detected, China may refuse to extradite the individuals involved. Still, these controls can be effective, and carry fewer risks than deemed export controls, so should be applied to critical advanced SME that China cannot produce. One
particular acute risk is that non-Chinese producers can aid the development of China’s photolithography industry by transferring currently uncontrolled technical data relating to photolithography tools that are less advanced than EUV tools, such as argon fluoride immersion photolithography tools.  

Deemed exports have the advantage of preventing workers from acquiring technical data in the first place, but they could also have adverse effects depending on how they are applied. As noted previously in this paper, transfers of tacit know-how are a critical factor determining the rate of China’s progress. Deemed exports target precisely these knowledge transfers: under U.S. law, a deemed export occurs when a foreign national acquires controlled technical data or source code, even if they are in the United States at the time. Accordingly, deemed export controls can prevent foreign nationals from accessing controlled technical data or source code while working for U.S. companies.

However, deemed export controls should be as narrowly tailored as possible to reduce technology transfer without deterring foreign nationals from working in U.S. industry. Deemed export license investigations are somewhat redundant with existing visa vetting processes. And broad deemed export controls would conflict with the U.S. technology sector’s cosmopolitan values. Therefore, exemptions should be introduced where possible. One option is to grant general licenses to U.S. employers that institute sufficient security measures. Additionally, if policymakers expand controls to new SME, they should exempt experienced foreign nationals employed at U.S. SME firms from related deemed export controls, which may cause experienced Chinese engineers to leave when they would otherwise stay in the United States. Finally, deemed export controls could focus on advanced SME that China has not yet acquired, such as EUV tools.

Even well-targeted deemed export controls can be harmful if the U.S. Commerce Department lacks sufficient resources to process licenses quickly. In 2019, deemed export licensing delays reached nearly seven weeks on average for Chinese nationals with some delays as high as eight months. These delays can become de facto denials.
The United States and its allies should ensure that export controls cover critical, specialized components needed to produce advanced SME. This would serve as an insurance policy to prevent China from short circuiting its efforts to build SME by assembling imported components. Instead, China would be forced to indigenize these components before indigenizing SME, as it is currently attempting to do for photolithography tools. Like SME itself, many of the component parts of SME are built exclusively by firms in the United States and allied countries. This paper has previously noted that ASML’s EUV photolithography machines use mirrors sourced from German optics firm Carl Zeiss, laser amplifiers from Trumpf, a German equipment maker, and light sources from Cymer, an American firm. Each of these technologies is highly sophisticated and would take time for a Chinese competitor to replicate.

The effectiveness of export controls on components will depend on implementation. In general, multilateral or plurilateral controls will be more effective. For example, if the United States enforces export controls on optics but Japan refuses to follow suit, Japanese firm Gigaphoton could plausibly fill the void by developing EUV optics technology. Controls must also be enforced with a presumptive denial of export licenses for any end-use in Chinese SME. Licensing officers in the United States and allied countries typically approve export licenses for most semiconductor technologies, undercutting the effectiveness of controls.¹¹²

Additionally, while “specially designed” components for controlled SME—which include EUV tools—are currently controlled,¹¹³ the United States and its allies should interpret these controls to cover

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### Critical equipment components

<table>
<thead>
<tr>
<th>Policies</th>
<th>Government subsidies</th>
<th>Imports of SME components</th>
<th>Explicit knowledge transfers</th>
<th>Tacit know-how transfers</th>
<th>Collaboration between Chinese SME firms and fabs</th>
</tr>
</thead>
<tbody>
<tr>
<td>Impose multilateral export controls on critical SME components</td>
<td></td>
<td>✓</td>
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</tr>
</tbody>
</table>

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EUV light sources, laser amplifiers, and optics. They should also further study whether additional controls are merited on components that are currently uncontrolled, which may include unspecialized components or components specially designed for uncontrolled SME.

The United and its allies can also consider end-user controls applied to Chinese SME firms. Military end-user export controls potentially already apply to AMEC.114

One risk of these controls is if China succeeds in indigenizing controlled components it would otherwise have imported and Chinese SME firms incorporate these components and gain market share. In this scenario, non-Chinese component suppliers could suffer sales losses, harming their ability to reinvest profits to sustain their competitiveness. Controls on components will work best if they are difficult for China to indigenize or if U.S. and allied SME firms can continue to outcompete Chinese SME firms in the Chinese market, despite the latter’s access to indigenous components.

### Equipment

<table>
<thead>
<tr>
<th>Accelerants</th>
<th>Government subsidies</th>
<th>Imports of SME components</th>
<th>Explicit knowledge transfers</th>
<th>Tacit know-how transfers</th>
<th>Collaboration between Chinese SME firms and fabs</th>
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<tbody>
<tr>
<td>Policies</td>
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<td></td>
<td></td>
</tr>
<tr>
<td>Impose multilateral export controls on equipment</td>
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<td></td>
<td>✓</td>
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</tbody>
</table>

The United States and its allies should coordinate export controls on advanced, linchpin SME that China has not already developed or imported to reduce the risk of Chinese SME firms reverse-engineering advanced SME. Additional controls on linchpin SME that Chinese firms have already acquired could be desirable as well, as they will slow China’s progress toward the leading edge in chip manufacturing, although they will do less to reduce the transfer of explicit SME knowledge. Countries participating in multilateral or plurilateral export controls should also apply controls extraterritorially to prevent firms headquartered in member
countries from moving production to countries not applying export controls.\textsuperscript{115}

Export controls often spur the indigenization of strategically important technologies within countries targeted by controls, while harming the competitiveness of U.S. firms. For example, export controls on U.S. satellite technologies likely contributed to the decline of the U.S. space industrial base and the rise of globally-competitive space industries in other countries.\textsuperscript{116}

If carefully-targeted, however, export controls can slow indigenization. The previous section of this paper argued that access to finished SME for inspection and reverse-engineering is a source of explicit knowledge. While it is exceptionally difficult to reverse-engineer the most complex modern SME, careful inspection of leading-edge devices would likely help, just as it likely helped China to (imperfectly) replicate American F-22.\textsuperscript{117} Thus, for EUV photolithography machines and other advanced SME China has never acquired, export controls would cut off a source of explicit knowledge, hindering China’s reverse-engineering efforts.

By contrast, export controls on SME that China has already acquired in significant quantities have more ambiguous effects. On one hand, such controls would slow the creation or development of China’s leading-edge chipmakers, depriving Chinese SME firms from collaboration and revenue sources. On the other hand, controls may bind Chinese SME firms and Chinese chipmakers closer together, perhaps accelerating the formers’ progress in the long term. The case of Japan elucidates this risk. In the 1970s, Japan’s protectionist policies ensured Japanese fabs would buy tools from Japanese SME suppliers. This gambit paid off; in the early 1980s, Japanese chipmakers beat U.S. chipmakers to the market with 64-bit DRAM memory chips, expanding both the Japanese memory chip and SME industries.\textsuperscript{118}

Even if China already has access to a given SME for reverse-engineering, export controls on that SME may still be desirable due to their effects on China’s chip manufacturing capabilities. As argued in other CSET research,\textsuperscript{119} export controls on certain advanced SME required for advanced chip manufacturing—such as
tools for ion implantation, atomic layer etch, and EUV and ArF immersion photolithography—would slow the progress of China’s leading chipmakers. Indeed, one goal of slowing China’s SME indigenization is to ensure that export controls on SME remain effective in slowing China’s chipmaking progress, thereby sustaining China’s import dependence on the United States, South Korea, and Taiwan for leading-edge chips.

**Prioritize assessment of SME component chokepoints and vectors of technology transfer**

<table>
<thead>
<tr>
<th>Accelerants Policies</th>
<th>Government subsidies</th>
<th>Imports of SME components</th>
<th>Explicit knowledge transfers</th>
<th>Tacit know-how transfers</th>
<th>Collaboration between Chinese SME firms and fabs</th>
</tr>
</thead>
<tbody>
<tr>
<td>Prioritize assessment of SME component chokepoints and vectors of knowledge transfer</td>
<td>✔️</td>
<td>✔️</td>
<td>✔️</td>
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</table>

As a first step toward controlling China’s access to critical SME components and SME-related technology transfers, the U.S. government should prioritize the assessment of SME component chokepoints, their value-add to finished SME, and vectors of SME technology transfers to China. Intelligence alone will do little to slow China’s SME indigenization efforts, but without a clear understanding of which SME components represent the most promising chokepoints, how much value these components contribute to finished SME, and which vectors of knowledge transfer are most problematic, policies aimed at slowing China’s indigenization of SME could be ineffective or even counterproductive.

Good intelligence is critical for formulating export controls that impact China’s efforts to indigenize advanced SME. As noted previously, export controls on certain SME components—inputs SME firms use to assemble finished SME—could help slow China’s indigenization of advanced SME. But identifying which components offer the most promising targets, and whether international cooperation on new export controls is necessary, is a
difficult analytic task, as SME firms may not disclose where they source their components from. Identifying firms that could offer substitutes for export-controlled products is a further challenge. Knowing how much value such components add to finished SME is also critical. For example, ASML’s EUV tools incorporate U.S.-origin technology (particularly EUV light sources), whose value-add would better inform the appropriate “de minimis” threshold for potential extraterritorial U.S. export controls on shipments of EUV tools from the Netherlands to China. (Under the de minimis rule, U.S. export controls apply to exports from one foreign country to another if an item includes a threshold percentage of U.S.-origin content controlled in the receiving foreign country.) All of this information is critical if export controls on SME components are to be effective.  

The same reasoning applies to policy efforts to reduce flows of SME-related explicit and tacit knowledge to China: a good understanding of China’s transfer efforts will help the United States formulate effective counter-transfer policies. To reduce the frequency and severity of SME IP theft, the United States and its allies would benefit from first understanding which technologies within the SME industry are most likely to be targeted by Chinese firms.

We offer three potential ways the U.S. intelligence community could prioritize assessment of SME components and vectors of technology transfer. First, the assessment of SME components and vectors of technology transfer should be prioritized in the National Intelligence Priorities Framework (NIPF). The NIPF informs National Intelligence Program planning, programming, and budgeting activities and the allocation of collection and analytic resources and can be updated on an ad hoc basis to reflect emerging priority issues. The NIPF should be updated to reflect the importance of the SME industry to China’s overall effort to gain semiconductor independence.

A second option for improving on current monitoring efforts is to establish an open-source National S&T Analysis Center. Relative to the United States, China currently has many more resources devoted to the collection of open-source intelligence (OSINT) on
global S&T developments. The United States intelligence community has historically focused on classified analyses of military affairs and technical collection, while marginalizing OSINT. However, open-source analysis is especially useful for monitoring fast-moving developments in advanced technologies.

However, intelligence-gathering efforts will do little good unless policymakers act to slow China’s SME indigenization efforts. The intelligence community should therefore communicate findings to the export control agencies; U.S. agencies responsible for cyber defense; and the Committee on Foreign Investment in the United States, which screens foreign investments in U.S. firms. Therefore, a final option to avoid this step is for the Bureau of Industry and Security to receive increased resources to perform aspects of this analysis on its own. Additional expertise within BIS—such as SME engineers and market analysts—could also improve BIS’s to more quickly adapt controls, including export licensing processing, to the fast pace of technological change in SME.

**Study and promote transparency on equipment subsidies**

<table>
<thead>
<tr>
<th>Accelerants</th>
<th>Government subsidies</th>
<th>Imports of SME components</th>
<th>Explicit knowledge transfers</th>
<th>Tacit know-how transfers</th>
<th>Collaboration between Chinese SME firms and fabs</th>
</tr>
</thead>
<tbody>
<tr>
<td>Policies</td>
<td>Consider challenging China’s SME subsidies</td>
<td>✓</td>
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</table>

As Beijing increases SME subsidies in the second round of the Big Fund and other funds do the same, the United States and its allies should carefully assess them and continue to press China for transparency on their size and nature. One study found that Beijing’s state subsidies to Chinese chipmakers reach as high as about 40 percent of firm revenues. China may eventually apply comparably large subsidies to its SME firms, which will likely struggle to reach the current leading edge without extensive financial support from Beijing.

Although China is highly unlikely to drop its subsidy program under any circumstances, the United States has multiple options to
attempt to achieve this outcome. First, the United States can consider challenging these subsidies at the World Trade Organization. However, there are risks to this approach: such a challenge may not succeed if there is insufficient evidence to prove that the subsidies are illegal under WTO rules, could take years to resolve, could provoke retaliation by China, and may fail to garner support from allies. A better approach may be for the United States to launch negotiations with China, with reduced export controls as a bargaining chip.

**Promote equipment innovation through R&D and workforce investments**

<table>
<thead>
<tr>
<th>Accelerants</th>
<th>Government subsidies</th>
<th>Imports of SME components</th>
<th>Explicit knowledge transfers</th>
<th>Tacit know-how transfers</th>
<th>Collaboration between Chinese SME firms and fabs</th>
</tr>
</thead>
<tbody>
<tr>
<td>Policies</td>
<td>Promote equipment innovation through R&amp;D and workforce investments</td>
<td></td>
<td></td>
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<td>✓</td>
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</tbody>
</table>

The United States must strengthen its funding for R&D, as well as its domestic workforce, in order to preserve its leadership position in the SME industry. As argued in the previous section of this paper, China’s SME firms currently face a Catch-22 as leading SME firms in the United States, Japan, and the Netherlands are not just better-resourced—they also benefit from collaboration with leading-edge fabs, which provide invaluable product feedback throughout the process of refining next-generation SME into commercializable products, as well as the revenues necessary for leading SME firms to invest in R&D. Investments in continued SME innovation help to ensure that American firms continue to reap the benefits that come with leadership in the SME industry, locking out Chinese SME firms from crucial feedback loops and revenue derived from leading global fabs. (The United States should avoid tariffs aimed at locking Chinese SME firms out from global markets —see Box 3.)

**R&D funding.** The U.S. government should fund pre-competitive R&D programs in partnership with U.S. SME firms, as well as allied...
SME firms impacted by export controls on SME. The IP generated by this funding could be licensed to all program participants. U.S. SME firms should also be given refundable tax credits for R&D and production. In 2019, the U.S. semiconductor industry spent $39.8 billion in R&D, while the U.S. government spent only $1.7 billion on semiconductor-specific R&D and $4.3 billion on semiconductor-related R&D. Public R&D has not much increased in decades, even while private R&D has grown considerably. An increase in public R&D spending—including for SME—could help sustain U.S. and allied competitiveness. is overdue. The CHIPS for America Act, included in the NDAA for Fiscal Year 2021, authorizes a National Semiconductor Technology Center to conduct R&D into SME. This Center should be sufficiently funded, and supplemented with additional R&D efforts.

**Workforce development.** The U.S. SME industry competes for and relies upon high-end talent—especially foreign-born. Promising options for strengthening the U.S. SME workforce include investing in research universities and STEM education, sustaining the Optional Practical Training program, avoiding country-based caps on green cards, and increasing the number of available employment-based visas.

**Box 3: Impacts of tariffs on semiconductor manufacturing equipment**

In 2018, the U.S. government announced tariffs on SME imports from China. Such tariffs could slow the growth of Chinese SME firms by depriving them of revenue and fabs’ product feedback to help SME firms go from a working prototype to a high-volume, high-yield, commercially-viable machine. Thus far, most Chinese SME firms have not managed to secure the cooperation of the world’s leading fabs, but this could change as Beijing focuses more resources on catching up in SME. For example, TSMC verified and began using AMEC’s etch equipment in 2018. Although TSMC does not use this equipment to etch the smallest features of these chips, access to the world’s leading contract chip manufacturer gives AMEC an advantage over its competitors, and demonstrates a high level of Chinese capability in etch relative to other SME sub-sectors.
However, such tariffs are risky and inadvisable for several reasons:

- Chinese SME firms currently gain little revenue from non-Chinese markets, making tariffs a solution in search of a problem.
- Tariffs shield top global SME firms from competition, relaxing pressure to innovate.
- Increased tariffs would escalate trade tensions, risking retaliatory Chinese tariffs.
- China’s new strategy of fabs experimenting with indigenous SME serves as a next-best substitute for Chinese SME firms gaining the business of leading chipmakers.
- Tariffs could increase costs for non-Chinese SME firms with manufacturing operations in China.
- Under WTO rules, the United States must claim national security concerns for import restrictions. How far countries can go with such claims is unclear. Historically, the WTO has not questioned national security defenses raised against WTO challenges to trade restrictions, but has recently begun giving more guidance. It is currently a grey area.

**Conclusion**

Japan in the early 1970s had virtually no presence in the SME industry. Within two decades, Japanese firms had joined U.S. firms at the leading edge across most SME sub-sectors, a position many of them still hold today.

Today, China hopes to replicate Japan’s success. For now, the SME industry is the weakest part of China’s semiconductor supply chain—but catching up to the leading edge is not impossible. Chinese SME firms currently have access to imports of some SME components and potentially billions of dollars in subsidies. They will also benefit to some degree from technology transfers from top global firms, including some degree of returnee talent. If Beijing is permitted to leverage these key inputs to SME progress, it may be sufficient to bring even China’s weakest SME sub-sector, photolithography, to the leading edge.
Policymakers have a range of options to slow China’s progress in SME. These options include export controls, investment controls, and promoting transparency on, and further studying, Chinese subsidies of SME firms. Findings also suggest the importance of strengthening the United States’ infrastructure for S&T analysis—as well as the need to complement technology protection with investments in R&D and workforce development that actively promote continued U.S. SME innovation.
Appendix A: CSET Expert Survey Results

CSET sent a survey to 150 individuals identified as experts in the semiconductor industry over two waves in September and December 2019. Seven experts completed the survey: a fairly typical response rate for surveys requiring substantial time investment. This report draws on respondents’ answers to four prompts; the sections below present the wording of each of four prompts, as well as respondents’ answers, in full.

Question 1

Please rate the importance of the following resources for developing an internationally competitive SME industry in China. Rate by assigning a numerical value to the following resources indicating relative importance for a combined total of 100. You have the option to use "other" to enter a relevant resource that is not listed.

- More investment/funding: ___
- Access to blueprints and designs: ___
- Access to devices for reverse engineering: ___
- Talent with technical know-how (i.e. implicit knowledge, experience): ___

Table 8. Responses to Question 1

<table>
<thead>
<tr>
<th>More investment/funding</th>
<th>Access to blueprints and designs</th>
<th>Access to devices for reverse engineering</th>
<th>Talent with technical know-how</th>
</tr>
</thead>
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<tr>
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<td>20</td>
<td>40</td>
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</tbody>
</table>

Question 2

Given current trends, how many years from now will China develop internationally competitive industries in the following areas? Please enter a numerical value or range.

- Etch equipment: ___
- Deposition equipment: ___
Photolithography equipment: ___

Table 9. Responses to Question 2

<table>
<thead>
<tr>
<th>Deposition equipment</th>
<th>Etch equipment</th>
<th>Photolithography equipment</th>
</tr>
</thead>
<tbody>
<tr>
<td>5 to 10</td>
<td>5 to 10</td>
<td>10 to 15</td>
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<tr>
<td>5</td>
<td>5</td>
<td>5</td>
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</tbody>
</table>

Question 3

Would a change in China’s access to money and funding alter your estimated timelines?
   A. Definitely yes
   B. Probably yes
   C. Probably not
   D. Definitely not

Table 10. Tallied responses to Question 3

<table>
<thead>
<tr>
<th>A. Definitely yes</th>
<th>B. Probably yes</th>
<th>A. Probably not</th>
<th>A. Definitely not</th>
</tr>
</thead>
<tbody>
<tr>
<td>1</td>
<td>1</td>
<td>5</td>
<td>0</td>
</tr>
</tbody>
</table>

Question 4

Would a change in China’s access to talent alter your estimated timelines?
   A. Definitely yes
   B. Probably yes
   C. Probably not
   D. Definitely not

Table 11. Tallied responses to Question 4

<table>
<thead>
<tr>
<th>A. Definitely yes</th>
<th>B. Probably yes</th>
<th>A. Probably not</th>
<th>A. Definitely not</th>
</tr>
</thead>
<tbody>
<tr>
<td>3</td>
<td>4</td>
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</tr>
</tbody>
</table>
Authors

Will Hunt and Dahlia Peterson, CSET Research Analysts, focused on research and writing for this report. Saif M. Khan, CSET Research Fellow, directed the project and contributed to research and writing. The authors contributed equally to this report and are therefore listed in alphabetical order.

Acknowledgments

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Endnotes

1 Semiconductors include computer chips, which account for 81 percent of global semiconductor sales, as well as optoelectronics (10 percent), sensors (3 percent), and discretes (6 percent). “WSTS Semiconductor Market Forecast Fall 2020,” World Semiconductor Trade Statistics, December 1, 2020, https://www.wsts.org/esraCMS/extension/media/f/WST/4820/WSTS_nr-2020_11.pdf.


In the semiconductor industry, new generations of chips are called nodes. According to Moore’s Law, the industry typically introduces a new node every two years with double the transistor density as the previous node. In recent years, Moore’s Law has begun to slow down. Prior to the 22 nm node, node names referred to the size of certain important features on a chip, such as the gate length of a transistor, and two chips at the same node typically had similar speed and power, even if they were made by different companies. After the 22 nm node, however, more exotic transistor shapes were introduced. Today, node names no longer refer to meaningful feature sizes. Thus, for example, Intel’s “10 nm” node is more comparable in sophistication to TSMC’s “7 nm” chip than to TSMC’s “10 nm” chip.


14 Khan et al., “The Semiconductor Supply Chain.”


16 Chinese SME firms gain revenue equal to 12 percent of the Chinese SME market. VLSI Research. Subtracting a limited set of known sales outside China by ASM Pacific and Hoson, we estimate the localization percentage is no greater than 8 percent, and likely lower.


These bottlenecks represent especially critical types of SME that Chinese firms cannot produce. Khan et al., “The Semiconductor Supply Chain.”

VLSI Research’s Chinese revenues and market shares for deposition, test, and assembly and packaging count data from ASM Pacific, which is headquartered in Singapore and listed on the Hong Kong stock exchange.


VLSI Research.

“Yield” is the industry term for the percentage of chips produced in a wafer that are functional.


A stepper “steps” the silicon wafer between different locations while keeping the photomask in the same position, so that the photomask pattern can be transferred to different locations of the wafer to print more chips. Scanners can move not only the wafer, but also the photomask. This has a number of advantages, including allowing the use of smaller and thus less expensive lenses.


33 VLSI Research.

34 VLSI Research; Pillai, “The Emergence of Tools Suppliers.”


36 Experts were only asked about only three SME sub-sectors. The survey did not ask about the process control sub-sector, though this is another area of weakness for China.

37 Consider a highly optimistic scenario for China’s photolithography sub-sector, in which SMEE grows at a rate of 60 percent per year over the next decade, perhaps with the help of government subsidies. Even at this rate of growth, which has only been sustained twice in the history of the sub-sector (first by Nikon and then by ASML), SMEE’s revenues would amount to just half the size of China’s domestic market for photolithography equipment. China is projected to have 24 percent of the world’s semiconductor manufacturing capacity by 2030, which suggests it should have a comparable share of the photolithography market at that time. Antonio Varas, Raj Varadarajan, Jimmy Goodrich, and Falan Yinug, “Government Incentives and US Competitiveness in Semiconductor Manufacturing” (Boston Consulting Group and Semiconductor Industry Association, September 2020), 7, https://www.semiconductors.org/wp-content/uploads/2020/09/Government-Incentives-and-US-Competitiveness-in-
Because Chinese chipmakers lag behind the leading edge, much of this demand will likely be for pre-EUV photolithography tools. Simply meeting this domestic demand, then, would be a significant feat, and would allow Chinese SME firms to grow considerably—even if they continue to fall short of the goals outlined in the Made In China 2025 Technical Roadmap.


39 Tax subsidies are another important source of financial support from the Chinese government. In July 2020, China’s State Council issued “Policies for Promoting the High-Quality Development of the Integrated Circuit Industry and Software Industry in the New Era,” which exempts Chinese SME firms—as well as semiconductor design, material, packaging, and testing companies—from corporate income taxes for two years, and from 75% of corporate income taxes for the following three years. The policy also halves the statutory tax rate for these firms. “The State Council’s Issues Concerning the Promotion of Integrated Circuit Industry and Notice of Several Policies for the High-Quality Development of the Software Industry,” The State Council: People’s Republic of China, August 4, 2020, http://www.gov.cn/zhengce/content/2020-08/04/content_5532370.htm.


42 Huang, “Government-Guided Funds in China.”


“Big Funds,” Xinhua.

52 For firms throughout the global semiconductor supply chain, state subsidies amount to less than fifty percent of revenues. Measuring Distortions, OECD. Thus, in the short term SMEE likely will not receive more than 20 million dollars in subsidies per year.


54 “Big Funds,” Xinhua.


57 The remaining two respondents said “Probably yes” and “Definitely yes.”


59 For example, Japan-based Gigaphoton is the most likely alternative supplier to Cymer.

The U.S. Commerce Control List and the Wassenaar Arrangement list “specially designed components” of EUV tools under ECCN 3B001 and Section 3.B.1, respectively.

For example, ASML may have set up exclusive agreements with many of its most specialized suppliers, such that ASML’s competitors (including those in China) cannot purchase these components even in the absence of export controls.


For example, a major hacking campaign targeting a wide range of intellectual property from seven Taiwanese chip firms has been linked to mainland China,

71 Six out of seven surveyed semiconductor industry experts indicated that each of these methods are typically used by semiconductor companies to prevent know-how transfers; one expert said cybersecurity measures are not frequently used. One expert additionally noted that semiconductor companies employ red-teaming to identify information security risks.


75 Gilli, “Why China Has Not Caught Up Yet.”


Gilli, “Why China Has Not Caught Up Yet.”

Complexity is difficult to define, let alone measure, but cost per unit is one crude metric. EUV photolithography tools cost more than $120 million per unit, while F-22 jets cost more than $300 million per unit. Because most SME equipment costs less than an EUV scanner, this suggests that most SME equipment is less complex than an F-22, though some may be within the same order of magnitude of complexity. Cost per unit correlates with the speed of diffusion of the technology. C. Wilson, A. Grubler, N. Bento, S. Healey, S. De Stercke, and C. Zimm, “Granular Technologies to Accelerate Decarbonization,” Science 368, no. 6486 (April 2020): https://science.sciencemag.org/content/sci/368/6486/36.full.pdf.


There is some chance that Chinese nationals working abroad use LinkedIn less frequently than those from other countries, as LinkedIn is not as widely used in China as it is in the United States and the Netherlands. This would lead to an underestimate of the proportion of Chinese workers at top SME firms. Our analysis is based on publicly accessible versions of LinkedIn.

This analysis excludes data from Tokyo Electron, a Japanese firm, because Japanese workers rarely use LinkedIn.

The top 5 SME firms by revenue—Applied Materials, ASML, Tokyo Electron, Lam Research, and KLA—capture 66.3 percent of the market. These 5 companies employ approximately 57,000 workers, implying a total SME employee headcount of about 84,000, not including Chinese SME firms, which capture 2 percent of the market.

To estimate the number of Chinese workers at these firms, this analysis assumes that those who attend Chinese universities are Chinese nationals: this biases our results upward slightly, though the effect is likely negligible. More
significantly, some workers at the four firms listed will not have LinkedIn accounts: this likely biases the results downward. Our analysis is based on publicly accessible versions of LinkedIn.


90 Based on publicly accessible analysis of LinkedIn.


92 Brown and Linden, Chips and Change, 167–168.

93 However, at least one Chinese SME firm has succeeded in contracting with TSMC. AMEC’s plasma etching tools are currently in use by TSMC at the 5 nm node. Lena Li and Jessie Shen, “AMEC 5nm Plasma Etching Tools Verified by TSMC,” DigiTimes, December 21, 2018, https://www.digitimes.com/news/a20181221PD207.html. While this is a significant accomplishment, AMEC’s tool likely is not useful for creating the finest features on 5 nm chips, such as FinFETs.


The Exon-Florio Amendment, passed in 1987, changed the powers and importance of CFIUS, which existed previously but had been a relatively obscure entity.


For example, 16 EU member states currently have investment screening policies and several others have plans to adopt such policies. European Commission, “List of Screening Mechanisms Notified by Member States,” last updated January 14, 2021, https://trade.ec.europa.eu/doclib/docs/2019/june/tradoc_157946.pdf.


105 Second Quarter Recommendations, National Security Commission on Artificial Intelligence.


107 Key SME chokepoints include tools for EUV photolithography, argon fluoride immersion photolithography, imprint lithography, e-beam lithography, laser lithography, resist processing, metrology, inspection, advanced ion implantation, advanced etching, advanced deposition, wafer and mask handling, and testing for advanced logic chips. Khan et al., “The Semiconductor Supply Chain.”


109 Hunt and Zwetsloot, “The Chipmakers.”

110 Deemed export controls may be useful for EUV tools even though they are made only in the Netherlands, because they are sold to chipmakers such as Intel, an American firm. However, in cases where China already has access to a given tool, such as ArF immersion photolithography equipment, deemed export controls may offer less value.

111 Saif M. Khan, “U.S. Semiconductor Exports to China: Current Policies and Trends” (Center for Security and Emerging Technology, October 2020), 19,
112 Khan, “U.S. Semiconductor Exports to China.”

113 See ECCN 3B001 of the U.S. Commerce Control List and Section 3.B.1. of the Wassenaar Arrangement.


Chinese military end-users export controls are subject to more stringent controls than other entities in China. For details, see Khan, “U.S. Semiconductor Exports to China.”

115 For example, the United States should use the de minimis and/or foreign-produced rule to ensure U.S. controls cover U.S. firms’ SME produced abroad, such as in Singapore, which is now a significant exporter of SME. Bown, “How the United States Marched the Semiconductor Industry,” 27.


118 Stowsky, “The Weakest Link.”

For example, the previous section identified EUV light sources supplied by the American firm Cymer as a potential chokepoint: losing access to this component would increase China’s struggles in developing a working EUV photolithography tool. But unilateral export controls on exports of Cymer’s light source to China could be counterproductive: unless Japan coordinates on export controls with the United States, the Japanese firm Gigaphoton might step in to fill the gap left by Cymer.


Relevant agencies responsible for export controls covering semiconductor technologies include the Bureau of Industry and Security at the Department of Commerce, the Defense Technology Security Administration at the Department of Defense, and the Directorate of Defense Trade Controls and the Office of Export Control Cooperation at the Department of State.

By comparison, non-Chinese chipmakers receive subsidies equal to less than five percent of their revenue. Measuring Distortions, OECD, 84.


130 Hunt and Zwetsloot, “The Chipmakers.”


132 Taiwanese chipmakers do purchase a few other Chinese SME tools; for example, NAURA’s deposition and etch tools and Hoson’s assembly and test tools. VLSI Research.

133 In one recent exception, the WTO adjudicated a national security defense raised by Russia to a challenge filed by Ukraine. In that case, the WTO found Russia’s national security defense to be sufficient to allow Russia’s restrictions on Ukrainian traffic in transit through Russia to Mongolia. “Russia — Measures Concerning Traffic in Transit,” World Trade Organization, DS512, https://www.wto.org/english/tratop_e/dispu_e/cases_e/ds512_e.htm.